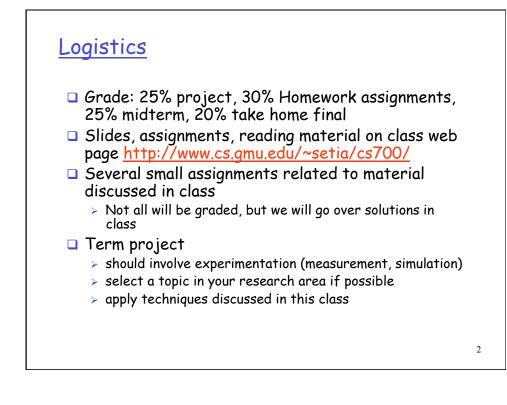
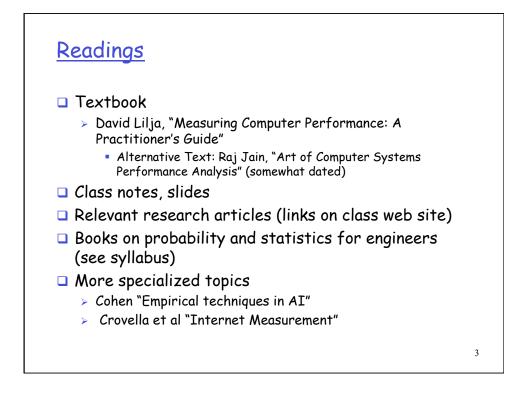
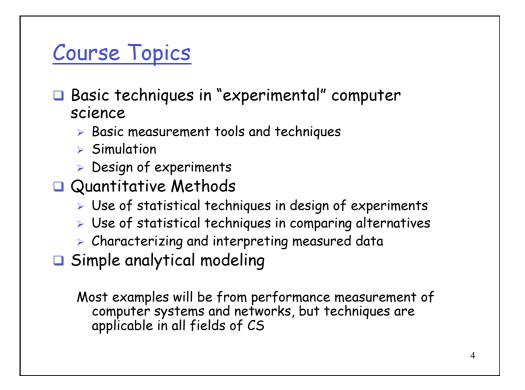
### <u>CS 700: Quantitative Methods &</u> <u>Experimental Design in Computer Science</u>

Sanjeev Setia Dept of Computer Science George Mason University





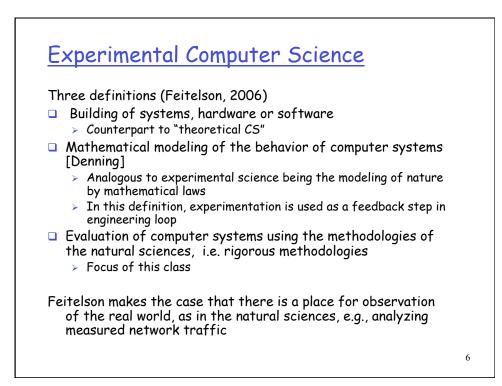


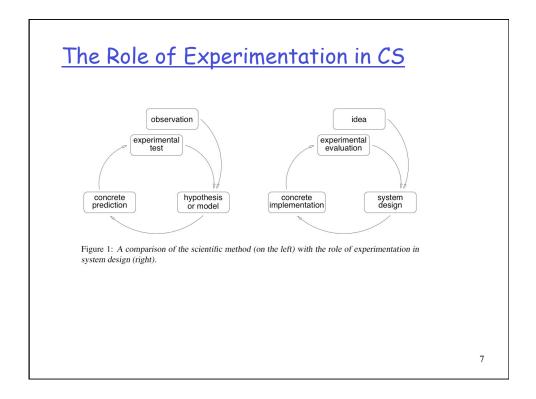
### **Experimental Science**

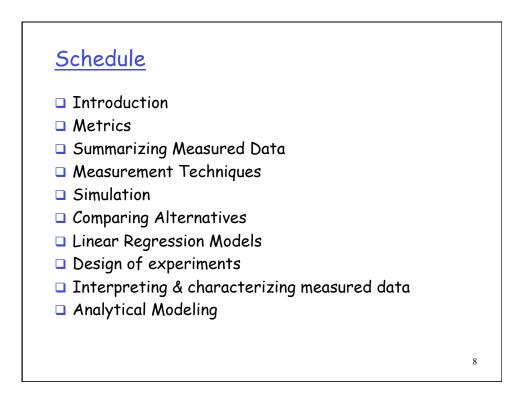
#### Scientific Method

- 1. Identify a problem and form hypothesis
  - Hypothesis must be testable and refutable
- 2. Design an experiment
- 3. Conduct the experiment
- 4. Perform hypothesis testing
  - Use statistical techniques

What about Computer Science?



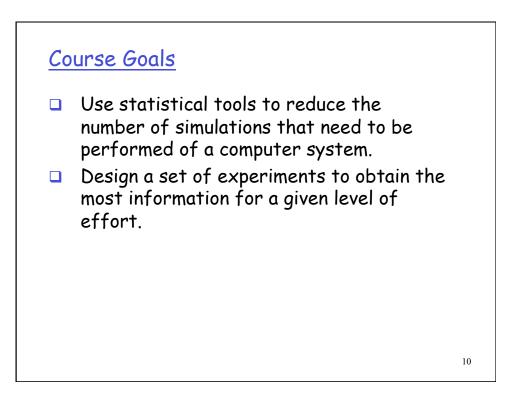






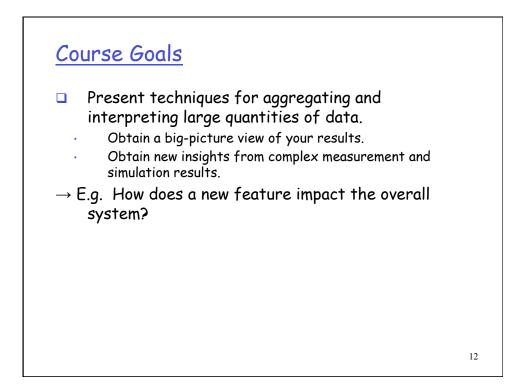
- Understand the inherent trade-offs involved in using simulation, measurement, and analytical modeling.
- Rigorously compare computer systems/ networks/software/artifacts/... often in the presence of measurement noise
  - Usually compare performance but in many fields of CS, "quality" of the output is more important than raw performance, e.g. face recognition software

 Determine whether a change made to a system has a statistically significant impact





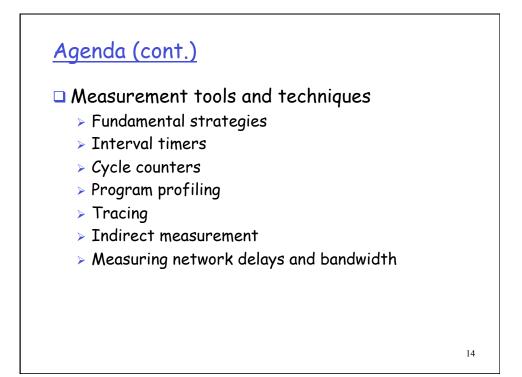
- Provide intuitive conceptual background for some standard statistical tools.
  - Draw meaningful conclusions in presence of noisy measurements.
  - Allow you to correctly and intelligently apply techniques in new situations.

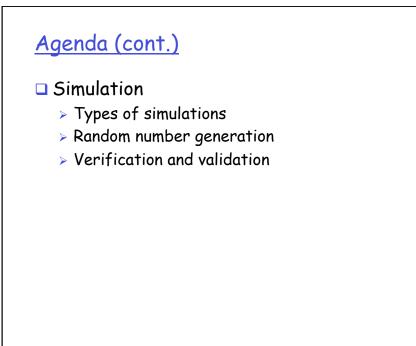


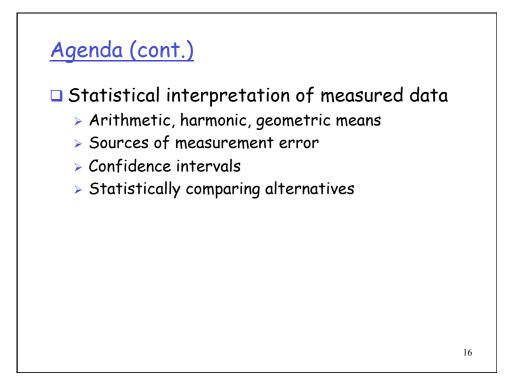
# Agenda

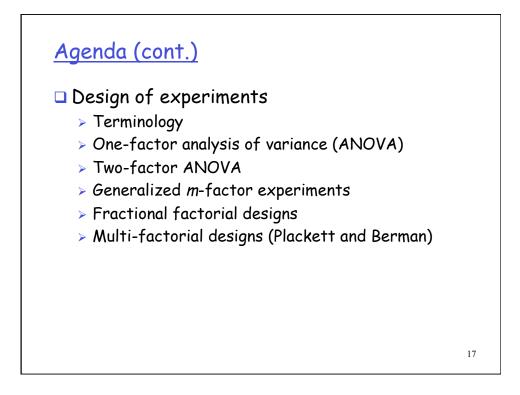
### Today

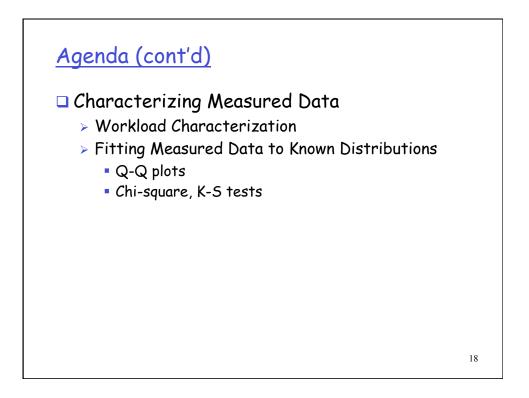
- > Overview of course
- > Performance metrics
  - Characteristics of good metrics
  - Standard processor and system metrics
  - Speedup and relative change

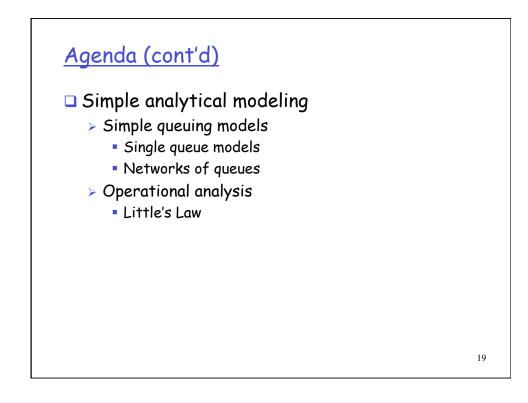


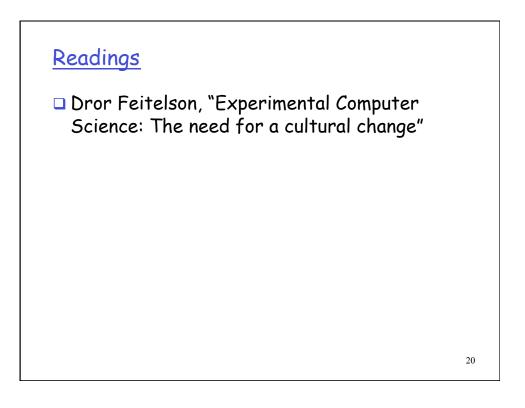


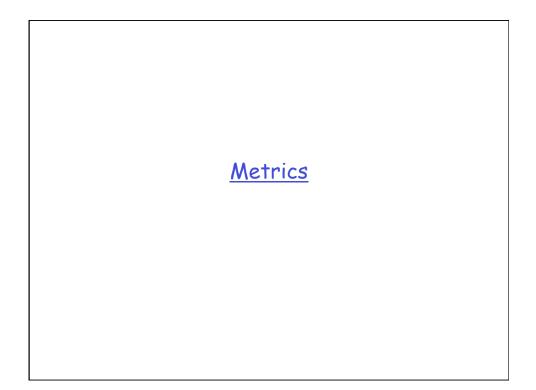


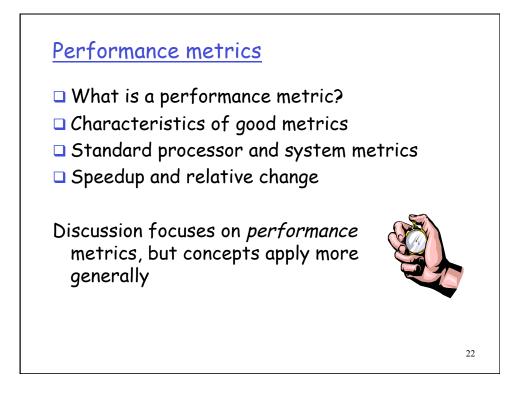


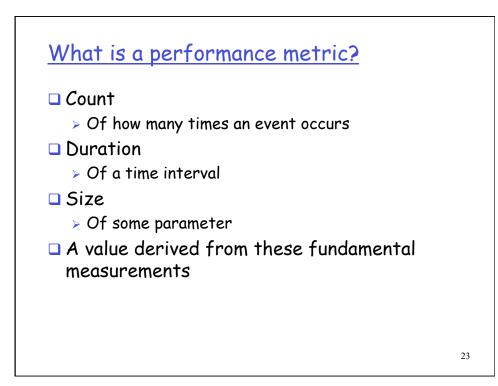


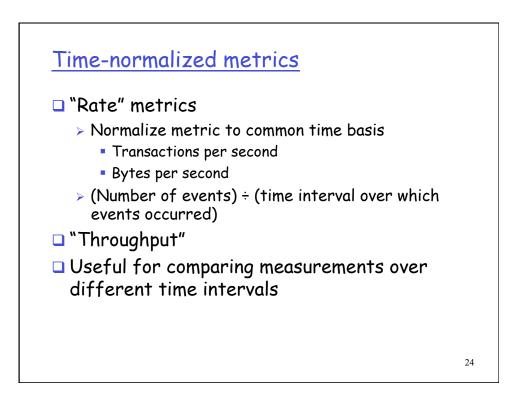


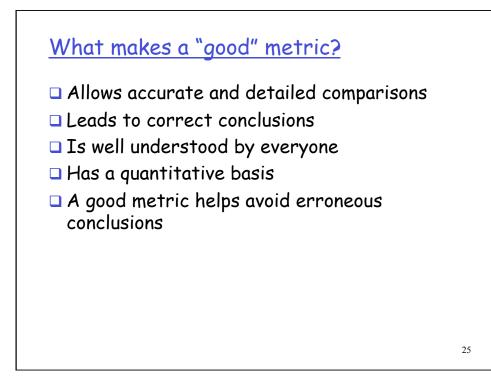


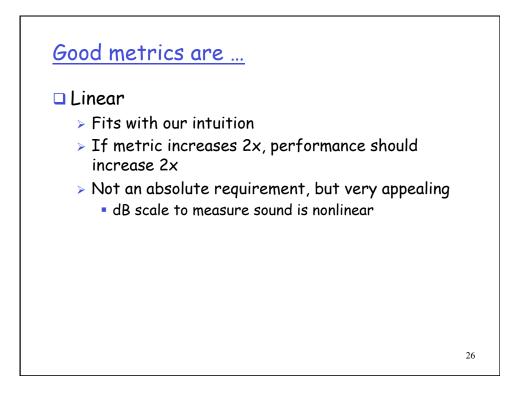


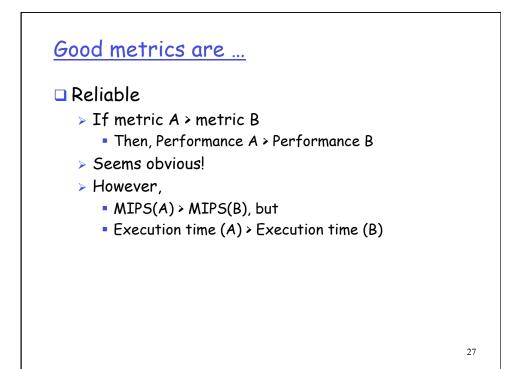


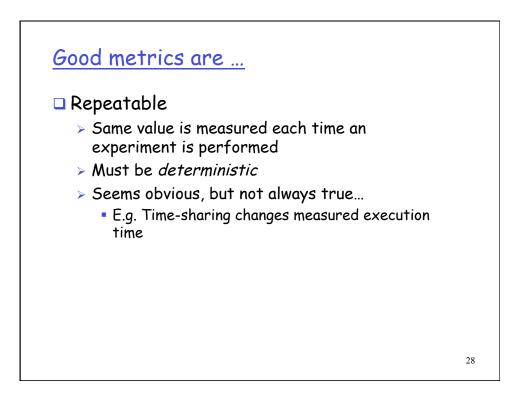


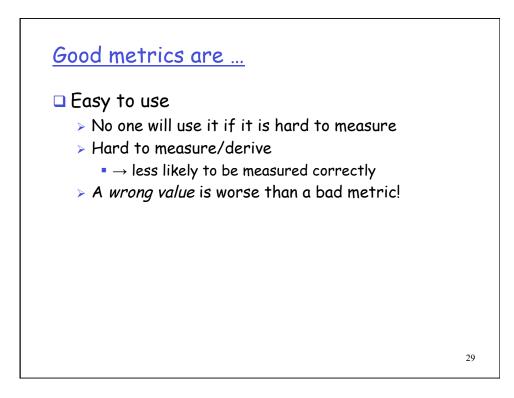


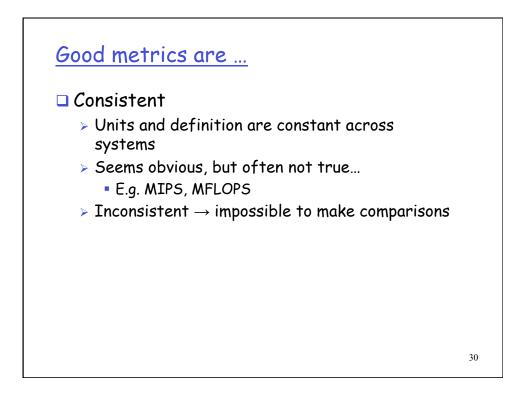


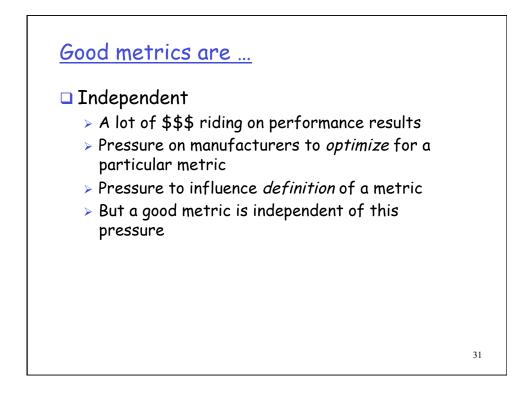


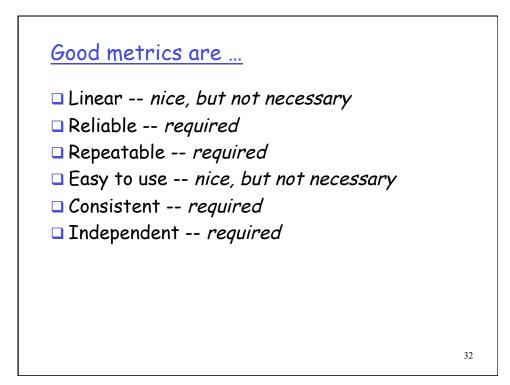






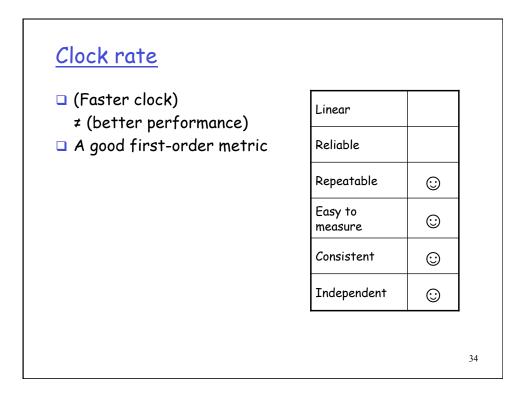






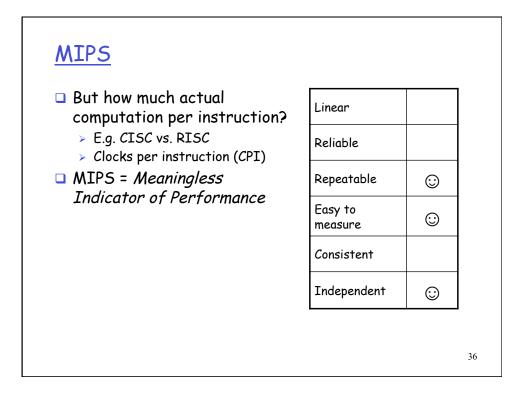


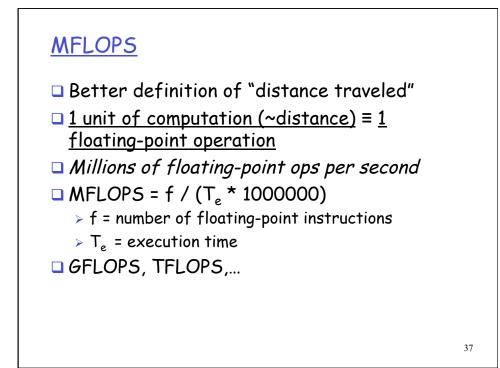
- Faster clock == higher performance
  - > 1 GHz processor always better than 2 GHz
- But is it a proportional increase?
- What about architectural differences?
  - > Actual operations performed per cycle
  - Clocks per instruction (CPI)
  - > Penalty on branches due to pipeline depth
- What if the processor is not the bottleneck?
  - > Memory and I/O delays

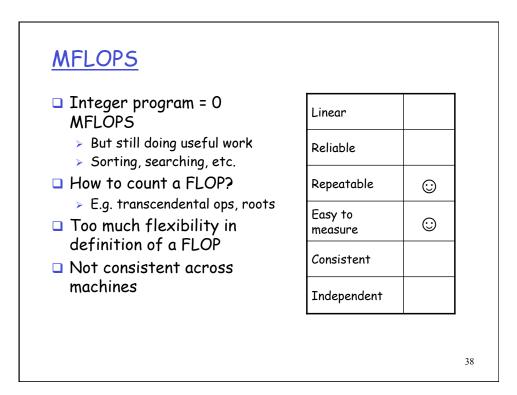


## <u>MIPS</u>

- □ Measure of computation "speed"
- Millions of instructions executed per second
- $\Box$  MIPS = n / (T<sub>e</sub> \* 100000)
  - > n = number of instructions
  - >  $T_e$  = execution time
- Physical analog
  - > Distance traveled per unit time





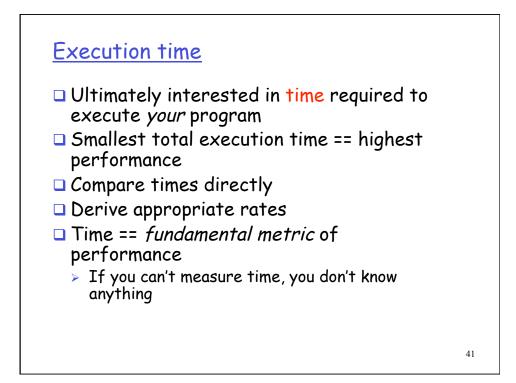


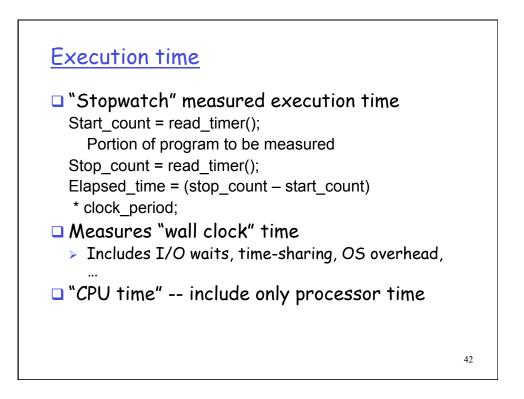
### <u>SPEC</u>

- System Performance Evaluation Coop
- Computer manufacturers select "representative" programs for benchmark suite
- Standardized methodology
  - Measure execution times
  - > Normalize to standard basis machine
  - SPECmark = geometric mean of normalized values

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#### SPEC Geometric mean is Linear inappropriate (more later) □ SPEC rating does not Reliable correspond to execution times of non-SPEC programs Repeatable $\odot$ Subject to tinkering Easy to <u>1</u>2 ⊙ > Committee determines which measure programs should be part of the suite Consistent $\odot$ > Targeted compiler optimizations Independent 40







- Best to report both wall clock and CPU times
- Includes system noise effects
  - Background OS tasks
  - Virtual to physical page mapping
  - Random cache mapping and replacement
  - Variable system load
- Report both mean and variance (more later)

Linear	$\odot$
Reliable	$\odot$
Repeatable	≈ :::
Easy to measure	÷
Consistent	©
Independent	©

	Clock	MIPS	MFLOPS	SPEC	TIME			
Linear					$\odot$			
Reliable					≈ ⊙			
Repeatable	$\odot$	$\odot$	$\odot$	$\odot$	0			
Easy to measure	©	$\odot$	$\odot$	<u>1</u> 2 ∵	$\odot$			
Consistent	©			$\odot$	$\odot$			
Independent	$\odot$	$\odot$			$\odot$			

